

**REMARKS**

The examiner has made the following restriction requirement:

Species 1, claims 34-40, 51, 55, 58 and 60 pertaining to a device having a plurality of active areas defined by STI having different widths with gate lines disposed thereon to create transistors having different threshold voltages; species 2, claims 44-48, 52, 56, 59 and 61 pertaining to a device with a sub-area having a width different from the one with which has a gate line extended therefore to define separate transistors; and Species 3, claims 49-50, 53-54, 57 and 62 pertaining to a device including a common gate line which extends over a plurality of sub-areas, each of which is no greater than one micron to create a plurality of transistors being joined in parallel to provide a pull down circuit.


Applicant elects Species 1, claims 34-40, 51, 55, 58, and 60 for prosecution on the merits.

Claims 44-50, 52-54, 56-57, 59 and 61-62 are canceled.

This application is now believed to be in immediate condition for allowance, and action to that end is respectfully requested. If the Examiner's next anticipated action is to be anything other than a Notice of Allowance, the undersigned respectfully requests a telephone interview prior to issuance of any such subsequent action.

Respectfully submitted,

Dated: 8-23-05

By:   
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